

## SYSTEM DESIGN AUTOMATION

### SDA DESIGN & SIMULATION

**SDA DESIGN & SIMULATION**  
EDActive  
IBM - Telelogic - iLogix  
MathWorks  
Agnisys  
Mentor  
MSC Software  
Mirabilis

## ARCHITECTURAL DESIGN

### ESL HARDWARE

#### ESL DESIGN & ENTRY

Synopsys  
Mentor  
CoFluent  
Atrenta  
Actis  
Agilent  
Agnisys  
Aldec  
Bellum  
Cadence  
Codetronix  
Doulog  
Easics  
Expressive Systems  
Handshake Solutions  
HyPerformix (SES)  
InterDesign Technologies  
MataiTech  
Mirabilis  
National Instruments  
Poseidon Design Systems  
Prosilog  
S2C  
Sankhya Technologies

#### ESL SYNTHESIS

Mentor  
Forte  
Bluespec  
Synopsys - Synfora  
ACE - Compaan Design  
Xilinx - AutoESL  
Axilica  
Cadence  
ChipVision  
Concurrent EDA  
Impulse Accelerated Technologies  
MathWorks  
NEC  
SystemCrafter  
Xilinx  
Y Explorations

#### SOC INTEGRATION PLATFORM

Beach  
Doulog  
Magellim  
Mataitech

#### SILICON VIRTUAL PROTOTYPE

Atrenta  
Cadence - Chip Estimate  
Chip Path  
Doulog  
ENTASYS  
Magillem  
Mentor  
ProximusDA

#### ESL SIMULATION

Mentor  
Aldec  
ARM  
Bluespec  
Carbon  
MathWorks  
Mesquite Software

#### ESL TARGET COMPILER

Agnisys  
ARRIS  
Arteris  
Atrenta  
Cadence - Denali  
Carbon - ARM  
Codasip  
Doulog  
iNoCs  
Inventure  
Mentor  
Mirabilis  
NISC  
PDTi (Productivity Design Tools)  
Semifore  
Silistix  
Sonic

#### APPLICATION ENGINE COMPILER

BINACHIP  
Critical Blue  
Mirabilis  
Poseidon  
Synopsys - CoWare - LISATek  
Target Compiler Technologies  
Tensilica

#### ESL POWER ANALYSIS

Apache - Sequence  
Mentor  
ChipVision  
Atrenta  
DOCEA  
Entasys  
Mirabilis

#### ACCELERATOR LIBRARY & DEVELOPMENT TOOLS

Forte - Arithmatica

#### ES LEVEL RF DESIGN

Agilent EEsof EDA  
AWR  
Ansys - Ansoft - Simec (Germany)

## ELECTRONIC SYSTEM LEVEL

### BEHAVIORAL

#### ARCHITECT'S WORKBENCH

MathWorks  
CoFluent  
Concurrent EDA  
IBM - Telelogic - iLogix  
Mentor - Summit  
Mirabilis  
MLDesign  
Space Codesign  
Synopsys - CoWare

#### NETWORK SIMULATION

ArchSite  
Synopsys - CoWare

## ARCHITECTURAL DESIGN

### ESL SOFTWARE

#### PARALLEL PROGRAMMING

ACE - Compaan  
Intel - CiLK Arts  
Connective Logic  
Cray  
Gedae  
IBM  
Intel - RapidMind  
MathWorks  
MBA Sciences  
NVIDIA  
Oracle - Sun  
Proximus  
Rocketick  
Sankhya  
Silicon Hive  
Vector Fabrics

#### ESL COMPILER

ACE  
CodePlay

#### ALGORITHM DEVELOPMENT & OPTIMIZATION

Synopsys  
Forte  
Critical Blue  
Sigmatix

#### MODEL DEVELOPMENT TOOLS

ARM - AxyS  
Carbon  
Embedded Systems Environment  
Infinscale  
Mentor  
Prosilog  
Synopsys - VaST

#### SOFTWARE SYNTHESIS

Mathworks  
Vayavya Labs (Vlabs)

#### MULTICORE DEVELOPMENT TOOLS

3L, Ltd  
Allinea Software  
American Arium  
ARM - Keil  
Axiom  
B Labs  
Cavium - Monta Vista  
Codeplay Software  
Critical Blue  
ENEA  
eXludas  
Express Logic  
First Silicon Solutions  
Green Hills  
Intel - Wind River  
Intelliasys  
Mathworks  
Mentor - CodeSourcery  
National Instruments  
Nohau Elektronik  
Plurality  
PolyCore Software  
QNX Software Systems  
RapidMind  
RoweBits Research  
Segger Micro  
Stream Processors  
Synopsys - Virage -  
ARC International  
TotalView Tech

#### SOFTWARE VIRTUAL PROTOTYPE

Synopsys  
Intel - Virtutech  
Imperas  
ARM  
ASTC  
Cadence  
Qemu

## ESL VERIFICATION

### BEHAVIORAL

#### INTELLIGENT TESTBENCH

Mentor  
Breker  
Cadence - Verisity - Inspec  
Springsoft - Certess  
NuSym  
Zocalo Tech

#### ESL TEST & VERIFICATION

Agnisys  
Avery  
Beach Systems  
Cadence - Verisity - Inspec  
Diagonal Systems (DS)  
IBM  
JEDA  
Levetate Design Systems  
MataiTech

#### ESL TEST & VERIFICATION

*(continued)*  
Mentor  
Semifore  
Source III  
SpringSoft - Novas Software  
Structural Design Verification  
Synapticad  
Temento  
TransEDA

#### ESL FORMAL VERIFICATION

Calypto  
MathWorks

#### TRANSACTION-BASED ACCELERATION & EMULATION

Cadence  
EVE  
Mentor

#### ESL CO-VERIFICATION

Cadence  
Mentor  
Tarek