

WHAT TO SEE @ DAC 2011

COMPANY	CATEGORY	BOOTH
Element 14	EDA Distribution	1221
OneSpin	RTL Sign-off - Formal Analysis	1505
Mentor	RTL Sign-off - SVP	1542
IC Manage	Design Management	1617
Atrenta	RTL Sign-off - SVP	1643
Magma	Analog Layout	1743
Docea	ESL Power	1912
Jasper	RTL Sign-off - Formal Analysis	1931
Calypto	RTL Power Design	2012
Vayavya Labs	Software Synthesis	2019
Oasys	RTL Sign-off - Synthesis	2031
SpringSoft	Design Debug	2043
Amiq	Silicon IDE	2114
edXact	Parasitic Analysis	2118
Asygn	Analog Simulation	2125
Real Intent	RTL Sign-off	2131
Cadence	SWVP	2237
BEEcube	Parallel Programming	2431
Apache	Power Analysis	2448
Physware	3D Extraction	2617
ATopTech	ASIC Layout	2816
Vennsa	Design Debug	2912
Rocketick	Parallel Programming	2914
Extreme DA	Timing Analyzer	2939
Chip Path	RTL Sign-off	3013
Silicon Frontline	3D Extraction	3017
NextOp	Design Debug	3131
Veridae	Post Silicon Validation	3212
Tuscany	Design Management	3349
Synopsys	RTL Sign-off - Synthesis	3433

www.garysmithEDA.com

contact **Gary Smith**
Mary Olsson

gary@garysmithEDA.com
mary@garysmithEDA.com