

## ELECTRONIC SYSTEM LEVEL

### BEHAVIORAL DESIGN

#### Architect's Workbench

MathWorks  
Intel - CoFluent  
Cadence - Jasper  
Concurrent EDA  
COSEDA Technologies (COSIDE)  
Intel - Docea Power  
IBM - Telelogic - iLogix  
Mentor - Summit

Mirabilis  
MLDesign  
ProximusDA  
Space Codesign  
Synopsys - Cadis

#### Network Simulation

ArchSile  
Synopsys - CoWare

## ARCHITECTURAL DESIGN

### ESL HARDWARE

#### ESL Design & Entry

Synopsys - CoWare - Atrenta  
Mentor Graphics  
Intel - CoFluent  
Actis  
Agnisys  
Aldec  
ARM - Duolog  
Bellum  
CA Technologies - Hyperformance (SES)  
Cadence  
Codetronix  
COSEDA Technologies (COSIDE)  
Easics  
Expressive Systems  
InterDesign Technologies  
Keysight EEsof  
MataiTech  
Mirabilis  
National Instruments  
Poseidon Design Systems  
Prosilog  
S2C  
Sankhya Technologies

#### High Level Synthesis (HLS)

Cadence - Forte - Chronology/Cynapps  
Mentor Graphics - Calypto  
Bluespec  
Synopsys - Target Compiler Technologies  
ACE - Compaan Design  
Aldec  
Algotochip  
Axilica  
Concurrent EDA  
Impulse Accelerated Technologies  
MathWorks  
NEC  
Space Codesign  
SystemCrafter  
Xilinx  
Y Explorations

#### SoC Integration Platform

Synopsys - Atrenta  
Cadence - Chip Estimate  
ARM - Duolog  
Magillem  
Mataitech  
Parallel Engines  
Sapient-IC

#### Silicon Virtual Prototype

Chip Path  
ENTASYS  
Magillem  
Mentor Graphics  
ProximusDA  
Synopsys - Atrenta  
Xilinx

#### ESL Power Analysis

Mentor Graphics  
ANSYS - Apache - Sequence  
Synopsys - Atrenta  
Intel - Docea Power  
Entasys  
Mirabilis

#### ESL Simulation

Cadence  
Synopsys  
Aldec  
ARM - Carbon Design  
ASTC/VLAB  
Bluespec  
COSEDA Technologies (COSIDE)  
Imperas  
MathWorks  
Mentor Graphics  
Mesquite Software  
Xilinx

#### ESL Target Compiler

Agnisys  
ARM - Duolog - Carbon Design  
Cadence - Denali  
Codasip  
iNoCs  
Inventure  
Memoir  
Mentor Graphics  
Mirabilis  
NISC  
PDTi (Productivity Design Tools)  
Semifore  
Silistix  
Synopsys - Atrenta  
System RDL

#### Application Engine Compiler

BINACHIP  
Cadence - Tensilica  
Critical Blue  
Mirabilis  
Poseidon  
Synopsys - CoWare - LISATek  
Sankhya

#### ESL Thermal Analysis

Intel - Docea Power

#### Accelerator Library & Development Tools

Cadence - Forte - Arithmatica

#### ES Level RF Design

Keysight EEsof  
National Instruments - AWR  
ANSYS - Ansoft - Simec  
COSEDA Technologies (COSIDE)

#### Platform IP

ARM - Sunrise Micro Devices  
Imagination - MIPs  
Sonics  
Arteris  
Cadence - Tensilica  
CEVA  
Synopsys

### ESL SOFTWARE

#### Parallel Programming

ACE - Compaan Design  
Cadence - Rocketick  
Connective Logic  
Cray  
CriticalBlue  
Gedae  
IBM  
Intel - CiLK Arts  
MathWorks  
MBA Sciences  
NVIDIA  
Oracle - Sun  
ProximusDA  
Sankhya  
Silicon Hive  
Vector Fabrics

#### ESL Compiler

ACE  
CodePlay  
Synopsys - Target Compiler Technologies

#### Algorithm Design & Optimization

Synopsys  
Cadence - Forte  
COSEDA Technologies (COSIDE)  
Critical Blue  
Sigmatix

#### Model Development Tools

ARM - Carbon Design - Axys  
Mentor - SpiraTech  
Agnisys  
ASTC/VLAB  
Bluespec  
CircuitSutra  
COSEDA Technologies (COSIDE)  
Embedded Systems Environment  
Imperas  
Infiniscale  
Intel  
Prosilog  
Synopsys - Target Compiler Technologies

#### Software Synthesis

Agnisys  
COSEDA Technologies (COSIDE)  
M2B  
Mathworks  
Vayavya Labs (Vlabs)  
Verum

#### Embedded Software Development Tools

ARM - Keil  
Mentor - CodeSourcery  
3L, Ltd Allinea Software  
American Arium  
AMIQ  
Argon  
Asset Intertech - Arium  
Axiom - SysChip  
B Labs  
Cadence  
Cavium - Monta Vista  
Codeplay Software  
Critical Blue  
Elbrus  
ENEA  
eXludas  
Express Logic  
First Silicon Solutions  
Green Hills  
Imperas  
Intel - Wind River  
Intelliasys  
Klocwork  
Lauterbach  
MathWorks  
National Instruments  
Nohau Elektronik  
Plurality  
PolyCore Software  
QNX Software Systems  
RapidMind  
RoweBits Research  
SECO  
Segger Micro  
Stream Processors  
Syntavisin  
TenAsys  
TotalView Tech

#### Software IP

ARM - Wicentric  
Broadcom  
Mentor Graphics  
Mirabilis Design  
Synopsys - Coverity

#### Software Virtual Prototype

Synopsys  
Intel - Virtutech  
Imperas  
Mentor Graphics  
ARM - Carbon Design  
ASTC/VLAB  
Cadence  
COSEDA Technologies (COSIDE)  
Qemu  
Space Codesign  
Xilinx  
Virtual Devive Technologies

#### Other Software

Synopsys  
Roadwave  
Imperas

## ESL VERIFICATION

#### Intelligent Test Bench

Mentor Graphics - Lighthouse  
Agnisys  
Breker  
Cadence - Verisity - Inspec  
COSEDA Technologies (COSIDE)  
Synopsys - Springsoft - Certess  
Zocalo Tech

#### ESL Test & Verification

Magillem  
Agnisys  
Avery  
Cadence - Verisity - Inspec  
COSEDA Technologies (COSIDE)  
Diagonal Systems (DS)  
ARM - Duolog - Beach Systems  
IBM  
JEDA Technologies  
Levitate Design Systems  
MataiTech  
Mentor Graphics

Semifore  
Source III  
Structural Design Verification  
Synapticad  
Synopsys - Springsoft - Novas  
Temento  
Xilinx

#### Transaction-based Acceleration & Emulation

Cadence  
Mentor Graphics  
Synopsys- EVE  
Aldec  
Tarek

#### ESL Formal Verification

Mentor Graphics - Calypto  
Cadence - Jasper  
COSEDA Technologies (COSIDE)  
MathWorks  
OneSpin